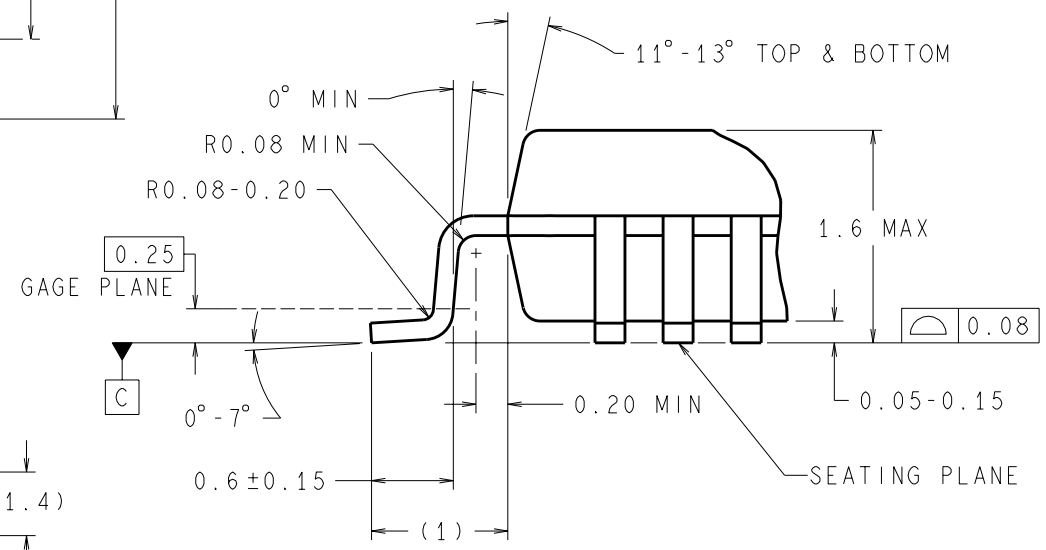
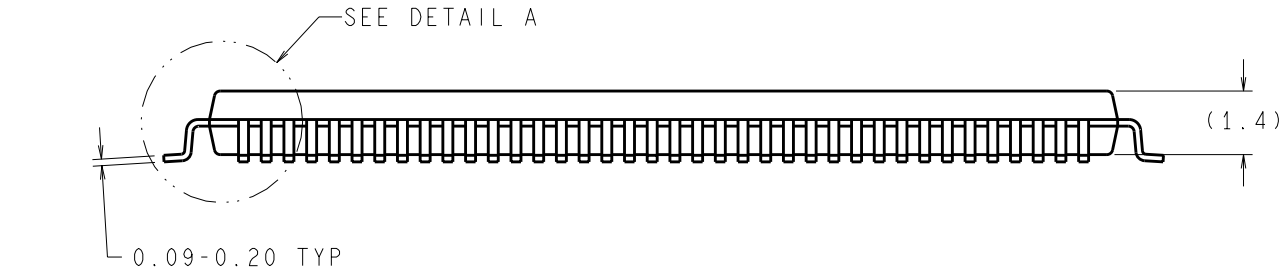
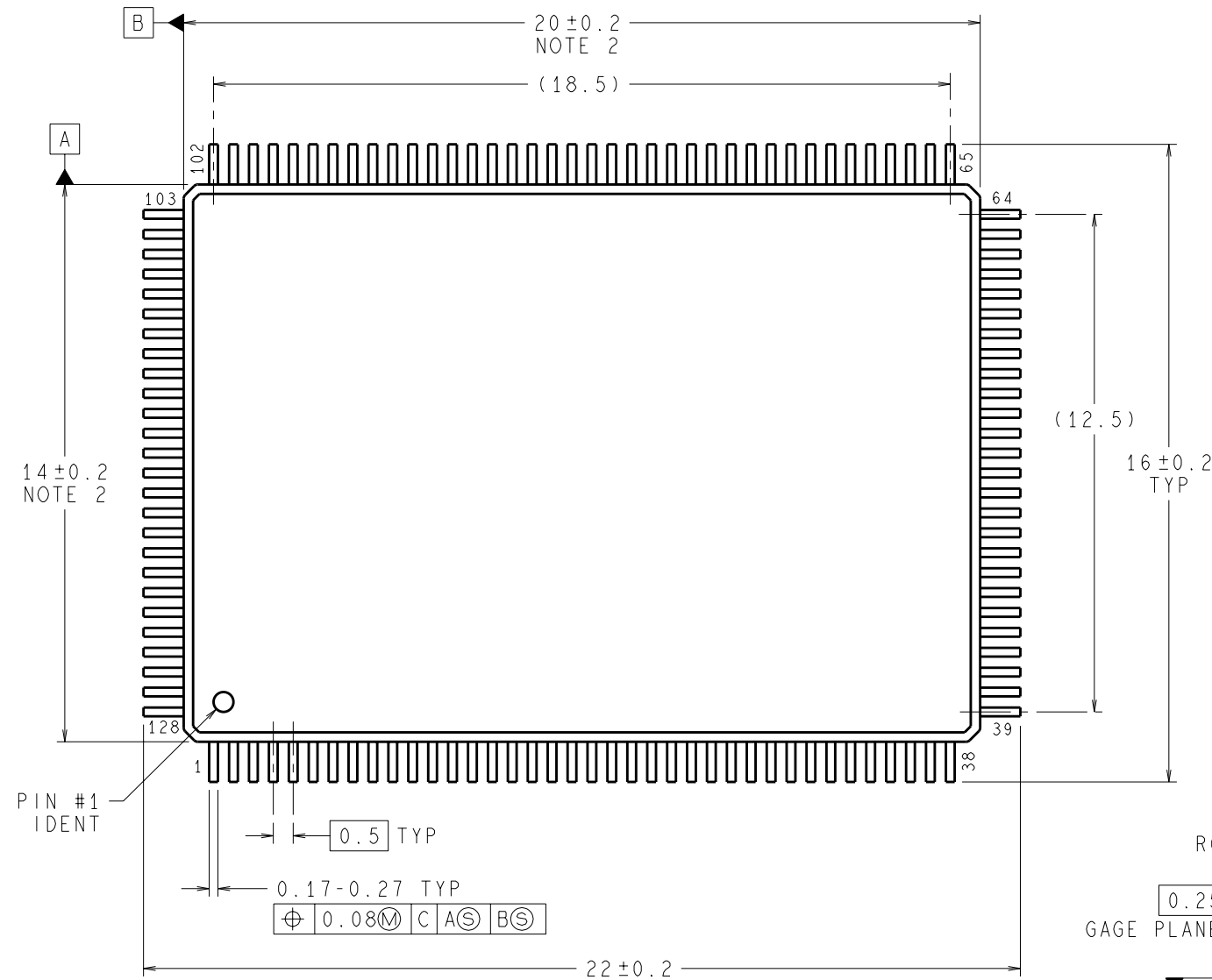


REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	12078	01/14/1999	MS/RW
B	TITLE: LQFP WAS TOFP; UPDATE NOTE 3	12317	11/09/1999	TL/RW
C	LD #38 WAS 32; LD #39 WAS 33; LD #102 WAS 96; LD #103 WAS 97.	12437	03/29/2000	MS/RW



**DETAIL A**  
TYP, SCALE: 30X

**DIMENSIONS ARE IN MILLIMETERS**

NOTES: UNLESS OTHERWISE SPECIFIED

- STANDARD LEAD FINISH:  
7.62 MICROMETERS MINIMUM SOLDER PLATING (85/15)  
THICKNESS ON COPPER.
- DIMENSION DOES NOT INCLUDE MOLD PROTRUSION.  
MAXIMUM ALLOWABLE MOLD PROTRUSION 0.25mm PER SIDE.
- REFERENCE JEDEC REGISTRATION MS-026, VARIATION BHB, DATED FEBRUARY 1999.

APPROVALS	DATE	National Semiconductor			
DRAWN <b>MARTA SUCHY</b>	07/10/1995	2900 Semiconductor Dr., Santa Clara, CA 95052-8090			
DFTG. CHK. <b>THANH LEQUANG</b>	03/31/2000	<b>LQFP, 14 x 20 x 1.4mm BODY, 128 LEAD, 0.5mm PITCH</b>			
ENGR. CHK. <b>RANDALL WALBERG</b>	03/31/2000				
PROJECTION		SCALE	SIZE	DRAWING NUMBER	REV
		N/A	C	(SC)MKT-VLB128A	C
INCH [MM]		FORMERLY: N/A	SHEET 1 of 1		